UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,762,737 B2

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DATED

APPLICATION NO.: 09/974910 : July 13, 2004

INVENTOR(S)

: Noriyuki Kajihara, Toshio Watanabe and Masafumi Katsutani

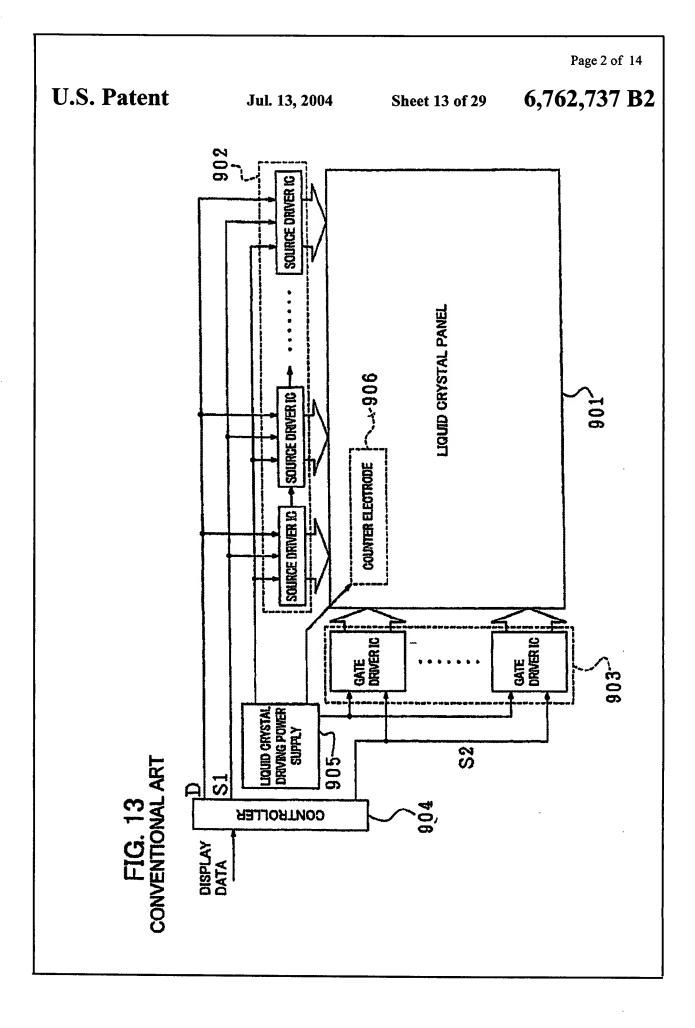
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

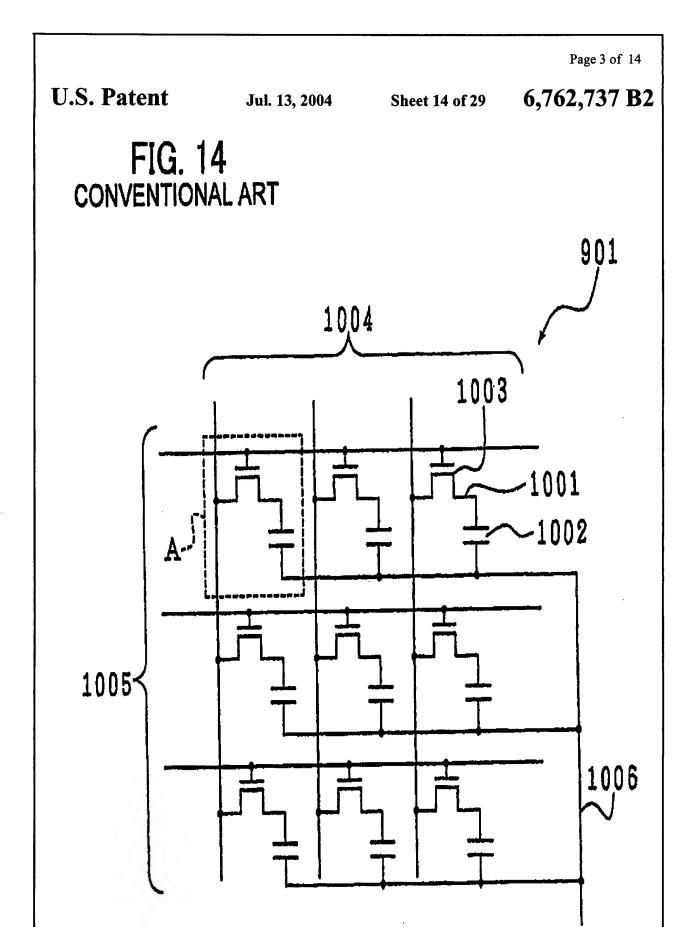
The sheets of drawings consisting of figures 13-26 should be deleted to appear as per attached figures 13-26.

Signed and Sealed this

Fourth Day of September, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office



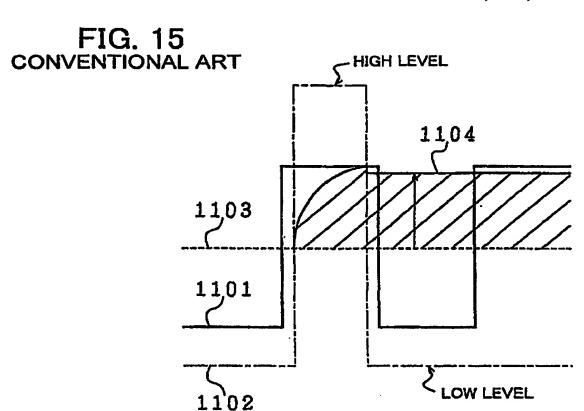


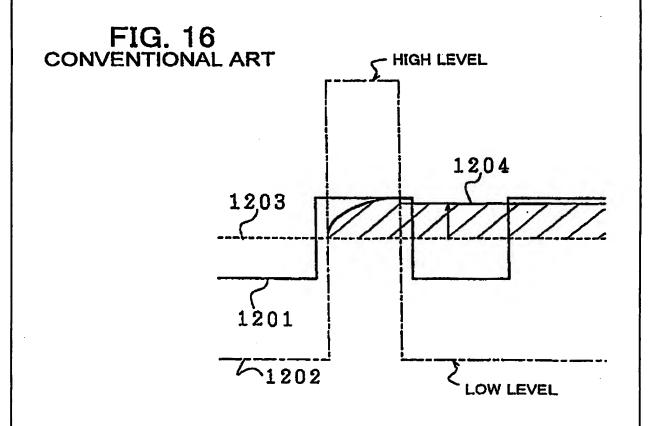
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LEVEL SHIFTER

DA CONVERTER

OUTPUT CIRCUIT

. . .

X1 Y1 Z1

x100 / 2100 1308

Y100

REFERENCE

VOLTAGE

GENERATOR

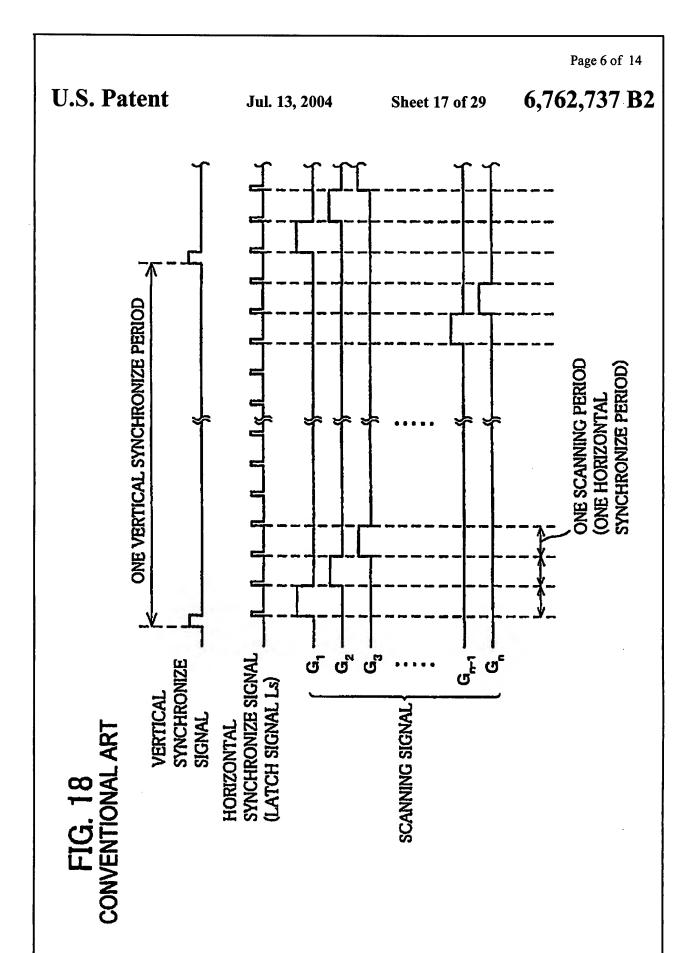
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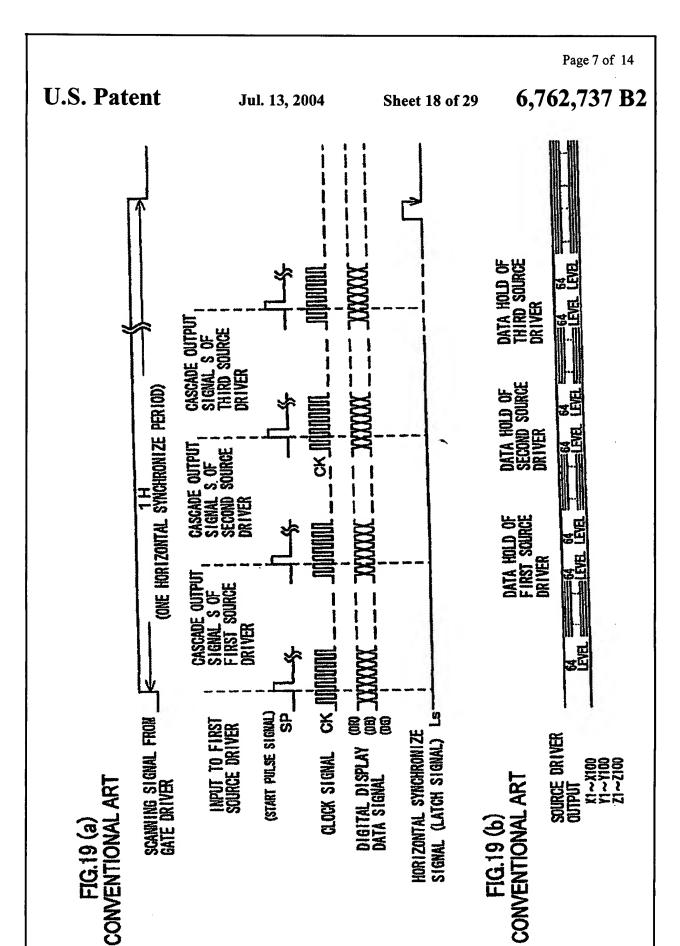
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FIG. 20 CONVENTIONAL ART

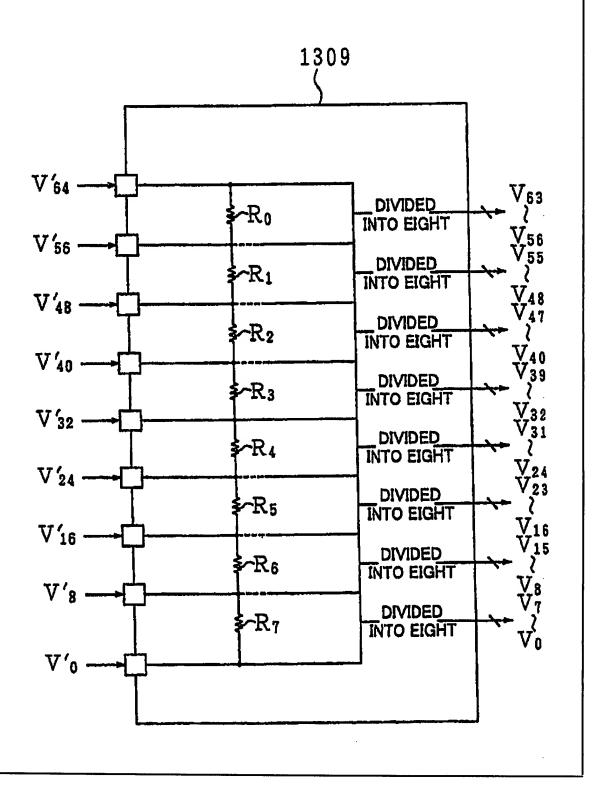
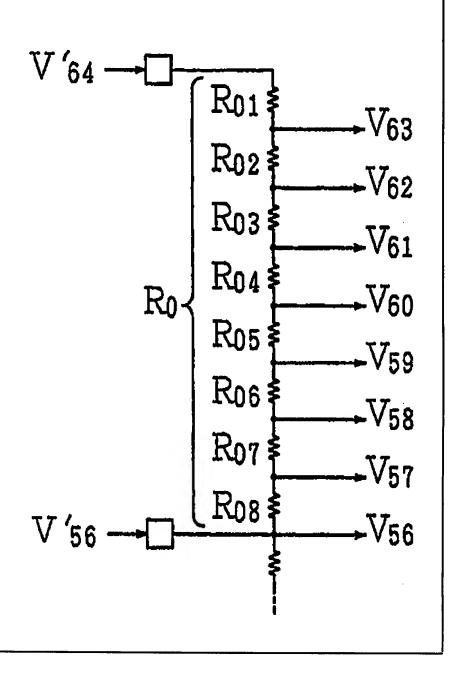
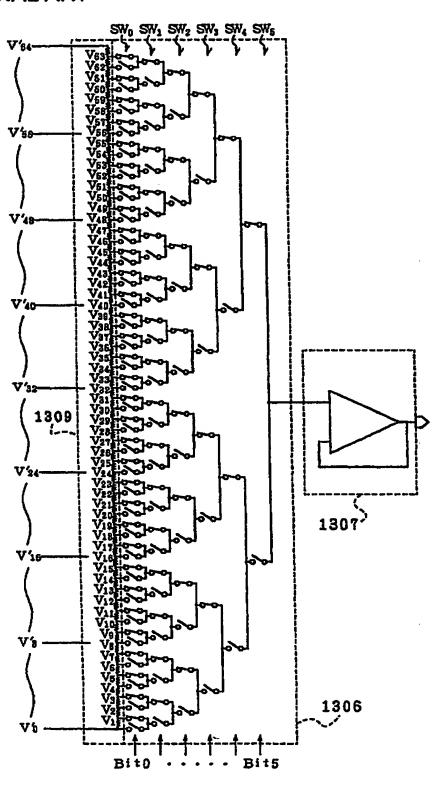


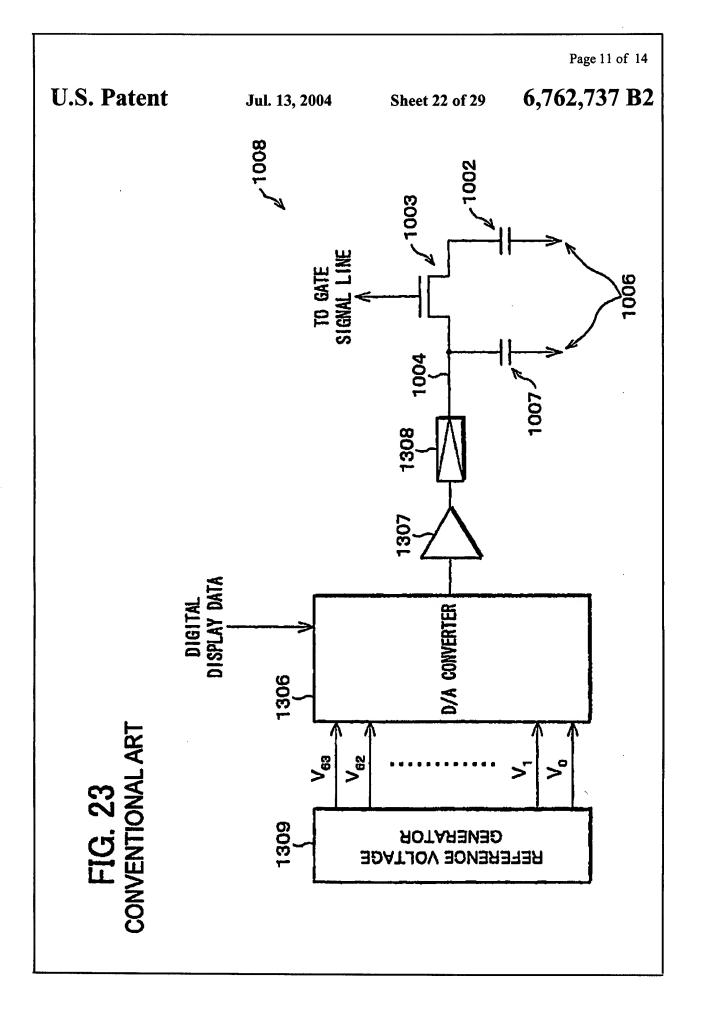
FIG. 21 CONVENTIONAL ART

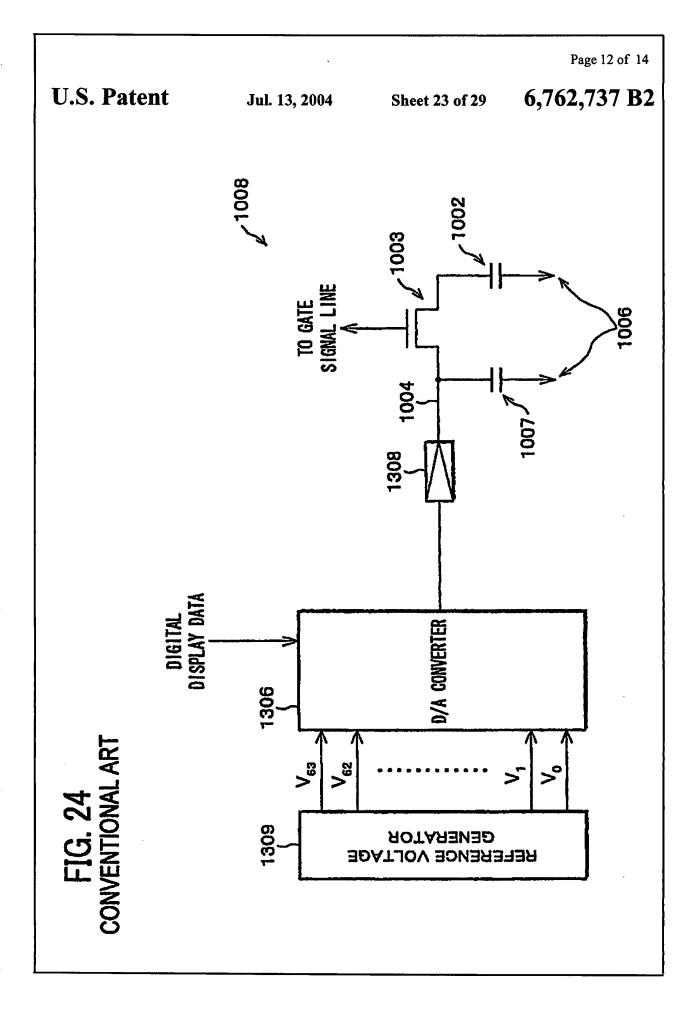


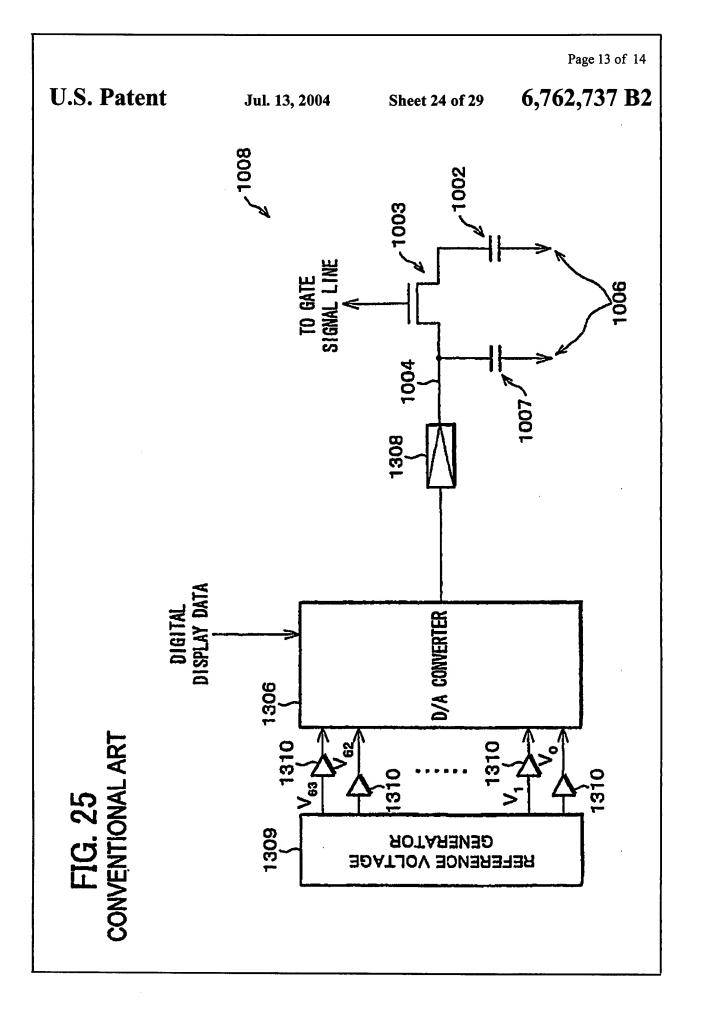
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FIG. 22 **CONVENTIONAL ART**









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FIG. 26 (a) CONVENTIONAL ART

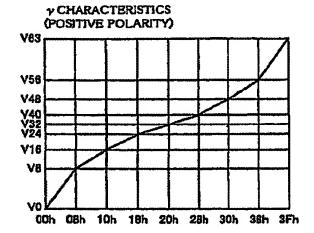


FIG. 26 (b) CONVENTIONAL ART

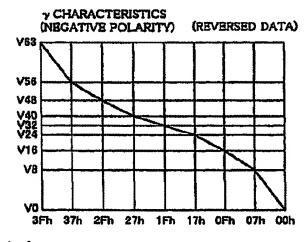


FIG. 26 (c) CONVENTIONAL ART

